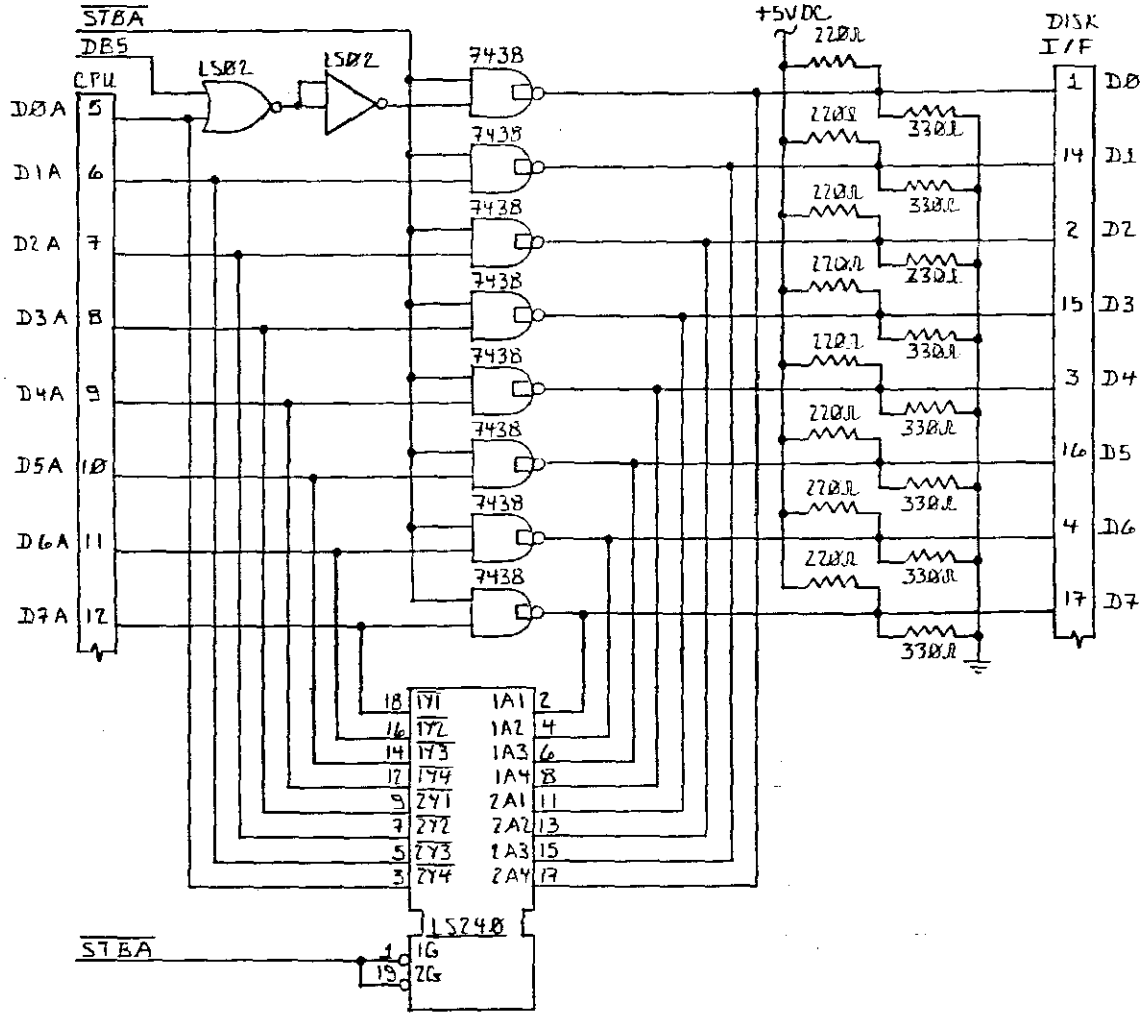
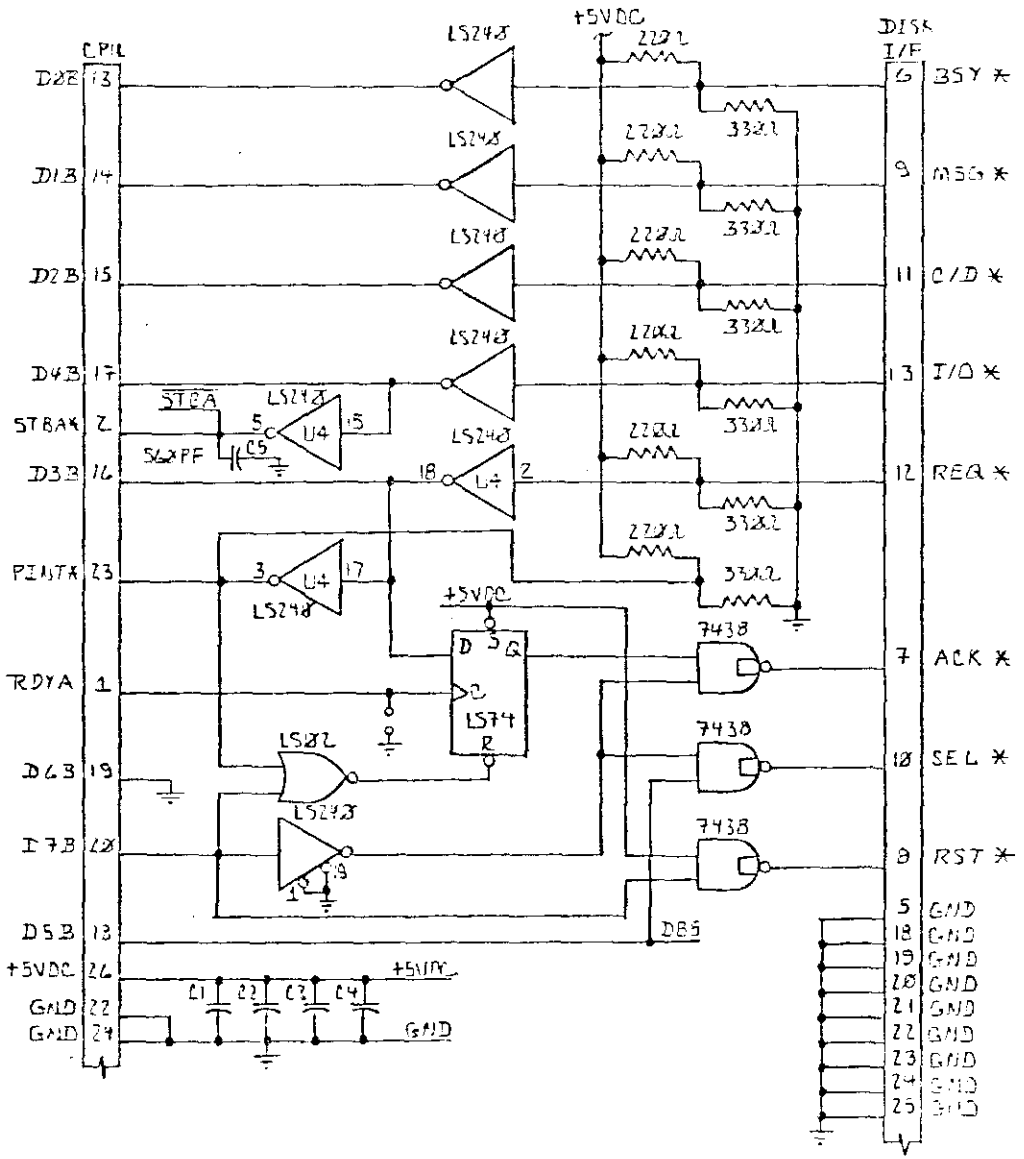
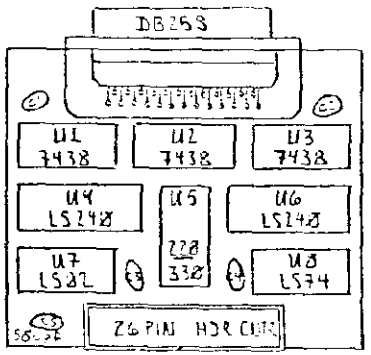


DESIGNED:	
REV: —	SCHEMATIC/LOGIC DIAGRAM FOR S.A.S.I. (SHUGART ASSO SYSTEM INTERFACE) PERSONALITY BOARD
	PART NO. SAS100
	SHT OF


CPU
DISK I/F
+5VDC
LS240
LS244
LS32
LS74
7438
D0B
D1B
D2B
D4B
STBA*
D3B
PINTA*
TRDYA
D6B
I7B
D5B
+5VDC
GND
GND
BSY *
MSG *
C/D *
I/O *
REQ *
ACK *
SEL *
RST *
GND
STBA
5K2PF
C5
C1
C2
C3
C4
DB6


DB25S
U1 7438
U2 7438
U3 7438
U4 LS240
U5 220 330
U6 LS240
U7 LS32
U8 LS74
26 PIN HDR CONN

SAS100